# Method and Means for Hypergeometric Function Calculation on an Array Processor 

Author(s): I BM TDB Grant, PM<br>IP.com number: IPCOM000054707D<br>Original Publication Date: March 1, 1980<br>Original Disclosure Information: TDB 03-80 p4699-4701<br>IP.com Electronic Publication: February 13, 2005

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## Method and Means for Hypergeometric Function Calculation on an Array Processor

This article describes a method for efficient calculation in the hardware of an array processor, of those hypergeometric functions which are defined by successive differentiation of a seed function. Examples are the Legendre functions, defined by:
$P(\mathrm{Im})(\mathrm{z})=(1-\mathrm{z} / 2 /) / \mathrm{m} / 2 / \mathrm{d} / \mathrm{m} / \mathrm{P}(\mathrm{I})(\mathrm{z})$ over $\mathrm{dz} / \mathrm{m} /$.
where $P(z)$ is the $l$-th Legendre polynomial; the associated Laguerre functions, defined by:
$\mathrm{L} / \mathrm{s} /(\mathrm{r})(\mathrm{Rho})=\mathrm{d} / \mathrm{s} /$ over d Rho/s/ L(r)(Rho).
where $L(P)$ is the 4-th Laguerre polynomial; and the spherical Bessel functions, defined by:
$j(n)(z)=z / n /(-1$ over $z d$ over $d z) / n / s i n z$ over $z$.
The attribute which permits the utilization of an array processor in the calculation of such functions resides in the fact that the seed function can be usually represented as a polynomial whose general term is simply:
$a(n) z / n /$.
with general derivative
See Original.
Thus, if in the calculation of the seed function polynomial either by recursion as in the case of Legendre and Laguerre polynomials, or by economization as for spherical Bessel functions, one forms an appropriate array of coefficients and exponents, the calculation of the required hypergeometric function becomes a trivial shift and multiply operation of this array in hardware capable of parallel array operations. Often the seed function itself can be calculated via such array manipulations, as, for example, when we define the Legendre polynomials by Rodriquez' formula: See Original.

In function ALF, lines 10-12 compute the coefficients and exponents of the seed Legendre polynomial by recursion. Lines 13-14 compute the required value of the associated Legendre function by the array manipulation algorithm discussed in this article.

The figure diagrammatically depicts the parallel processor architecture necessary for calculating hypergeometric function through application of Eqs. (4) and (5). The an, Zn , and n arrays are assumed to be previously loaded as is also the m -counter. The result will be accumulated in the n array. The n array is provided with a cumulative multiplier, that is, the product developed in the ith element is the product of $\mathrm{i}, \mathrm{i}-1, \mathrm{i}-2, \ldots, \mathrm{i}-\mathrm{m}+1$. The numbers 1,2 , and 3 emanating from the clock denote the timing sequence.


TIMING: 1 BEFORE 2 BEFORE 3

$$
\begin{aligned}
& L_{r}^{s}(\rho)=\frac{d^{s}}{d \rho^{s}} L_{r}(\rho), \quad j_{n}(z)=z^{n}\left(-\frac{1}{z} \frac{d}{d z}\right)^{n} \frac{\sin z}{z} \cdot \quad \frac{d^{m}}{d z^{m}} a_{n} z^{n}=a_{n}\left(\frac{n!}{(n-m)!}\right) z^{n-m} \\
& P_{\ell}(z)=\frac{1}{2^{\ell} \ell!} \frac{d^{\ell}}{d z^{\ell}}\left(z^{2}-1\right)^{\ell} .
\end{aligned}
$$

